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	HITA.0470	To be assigned 10/760,402
	APPLICANT	
	SEKIGUCHI et al.	
	FILING DATE	GROUP
	1/21/04	2818
Concurrently herewith		

#### U.S. Patent Documents

Examiner Initial		DOCUMENT NUMBER	DATE	NAME	CLASS	SUB-CLASS	FILING DATE
TP	XX	6,075,735	06/13/2000	Sugibayashi	365	189.11	10/01/1998
TP	X	5,050,126	09/17/1991	Tanaka et al.	365	230.03	10/03/1988
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#### Foreign Patent Documents

Examiner Initial		DOCUMENT NUMBER	FILING DATE	COUNTRY	CLASS	SUB-CLASS	TRANSLATION	
							YES	No
TP	XX	11-110967	10/01/97	Japan	—	—	Abstract	X
TP	XX	2002-93158	090/8/2000	Japan	—	—	Abstract	X

#### Other Documents (Including Author, Title, Date Pertinent Pages, Etc.)

XX	Satoshi Utsugi, Masami Hanyu, Yoshinori Muramatsu and Tadahiko Sugibayashi, "Noncomplimentary Rewriting and Serial-Data Coding Scheme for Shared-Sense-Amplifier Open-Bit-Line DRAM", IEEE Journal of Solid-State Circuits, Vol. 34, No. 10, October 1999, pp. 1391-1394
EXAMINER Trong Phan	
DATE CONSIDERED 5/15/04	
Examiner: Initial if citation is considered, whether or not citation is in conformance with MPEP 609; draw a line through citation if not in conformance and not considered. Include a copy of this form with next communication to applicant	
PTO 1449	

X Cited by Examiner in Parent  
XX Filed by applicant in parent